

•MCK  
MACHINE CHECK  
INTERRUPT  
FORCED ADDRESS.

QY001•NFE  
(XX)

XX 000C  
D PSW→S23  
C 0→STAD  
C 0→STAH  
R IC3=3  
A1 XX-AA  
CE OR IOCE  
MACHINE CK INTR  
INTERRUPT CODE  
TO S REG

•PGNY 0007  
L R→24→S  
C 0→STAG  
R STAC  
A3 XX-AC  
PAGING ADDRESS  
SPECIFICATION  
INTERRUPT BRANCH IF  
INTERRUPT WAS NOT  
FROM CSS INSTRUCTION.

X0 002C  
L R→GPB→Y  
A4 XX-AD  
CSS INSTRUCTION  
SORT RIN BASE TO T.

XX 0015  
A 0→T  
A 0→D  
S MS-REQ→D=3  
A5 XX-AE  
FETCH SQRV RIN BASE  
SAVE AREA.

XX 0016  
D MS→ST  
L W→GP13  
S STOP2  
A6 XX-AF  
RESTORE GPR13.

XX 0017  
A 0→S  
A 0→T  
L W→GP12  
C 0→IVSPEC  
A7 XX-AG  
RESTORE GPR12.  
SET SPEC ERROR.

•STPR 002F  
A9 XX-AI  
DELAY ON CPU  
STORE TO CHECK  
FOR STORAGE  
PROTECTION  
INTERRUPTION

•XINT 0006  
D PSW→S23  
C 0→STAD  
C 1→STAH  
R IC3=3  
E1 XX-EA  
EXTERNAL INTR  
INTERRUPT CODE TO  
S REG

X0 0350  
A IC+0-B  
A 0→T  
L W→24  
S MS-REQ→D=4  
C 3→STC  
C INH-MS-PROT  
R 0→DIERR  
F2 XX-FB  
D REQ STORAGE  
ADJUST IC VALUE  
BRANCH ON IOCE  
MCH CK INTR  
SAVE IC IN WORK  
REG

X0 0B50  
A 8+0-B  
A 0→AR3B  
D PSW→ST  
S SET-MARK=0-7  
C 0→STAG  
C 0→BR-INV-ADR  
R STAH  
F3 XX-FC  
STORE OLD PSW  
16 TO B REG  
RR ON CE MCH  
CHECK INTR

•SFCR 0008  
D PSW→S23  
C 0→STAD  
C 1→STAH  
R IC3=3  
G1 XX-GA  
SUPERVISOR CALL  
INTERRUPT CODE TO  
S REG

X1 0351  
A IC+0-16  
A 0→T  
L W→24  
S MS-REQ→D=4  
C 3→STC  
C INH-MS-PROT  
R 0→DIERR  
J2 XX-JB  
D REQ STORAGE  
ADJUST IC VALUE  
BRANCH ON IOCE  
MCH CHECK INTR  
SAVE IC IN WORK  
REG

XX 0B4F  
A 0→T  
B 1+0  
B 0→F  
D 0→EXTREG  
S STOP2  
J4 XX-JD  
PSBA TO EXT REG  
RESET T REG

XX 0B85  
B 1+U  
R 0→ST  
F 0→U  
C 0→INTR-GATE  
R 1  
J5 11-JE  
STC=6, TQN SELECT.  
02 HEX TO ST6.

XX 0B84  
B 0+U  
R 0→ST  
AB 0→U  
C STC+1  
J5 XX-JE  
ABC=3, STC=5  
INSERT BOX ADDR  
INTO PSBA 9-12

•PRNG 000A  
D PSW→S23  
C 0→STAD  
C 1→STAH  
S SET-MARK=0-7  
R IC3=3  
I1 XX-LA  
PROGRAM INTR  
INTERRUPT CODE TO  
S REG

X1 0947  
A IC+0-16  
A 0→T  
L W→24  
C 3→STC  
R 1  
N2 X1-NB  
ADJUST IC VALUE  
SAVE IN WORK REG

XX 0BA4  
B 0+0+U1  
B 0→ST  
AB 0→U  
C ABC+STC+1  
L3 XX-LC  
ABC=1, STC=3  
COMBINE BOX ADDR  
AND PART OF PSBA

XX 0BA5  
A 0+S  
A 0→R1→A  
C ABC+STC+1  
L4 XX-LD  
ABC=2, STC=4

XX 0B84  
B 0+U  
R 0→ST  
AB 0→U  
C STC+1  
L5 XX-LF  
ABC=3, STC=5  
INSERT BOX ADDR  
INTO PSBA 9-12

•IOIN 000F  
C 0→STAD  
C 1→STAH  
R IC3=3  
Q1 XX-QA  
I=0 INTERRUPT

X0 0946  
A IC+0-B  
A 0→T  
L W→24  
C 3→STC  
R 1  
Q2 X1-QB  
ADJUST IC VALUE  
SAVE IN WORK REG

X1 0B51  
L R→PSR→S→T  
S STOP1  
C 1→STAH  
C 0→ABC  
Q3 XX-QC  
I/O INTERRUPT OR  
IOCE MCH CK INTR.  
DELAY TO INSURE  
THAT PSBA IS OK.  
IF PSW STORE REQ.  
RESULTED IN SWITCH  
TO ALT. PSBA.

XX 0BA2  
A 0→TL1  
A 0→A  
B 0→V+0  
B 0→F  
C ARC+1  
Q4 XX-QD  
ABC=0, STC=3  
BOX ADDR TO  
F REG

XX 0BA3  
B 0+U1+0  
B 0→ST  
F 0→U  
Q5 XX-QE  
ABC=1, STC=3  
BOX ADDR TO LEFT  
HALF OF ST3

11 0B57  
A 0→T  
A 0→B  
R RELI/O  
J6 XX-JF  
RESTART TIMEDUT

10 0B56  
A 0→T  
A 0→B  
R RELI/O  
J6 XX-JF  
RESTART TIMEDUT

X0 0B52  
D PSW→S23  
R 0→RRS  
J47=0  
L7 XX-LG  
BR ON RESTART  
TIMEDUT OR COUNT=0.  
INTR CODE TO S REG

00 0B60  
A BL2+0  
D PSW→ST  
S SET-MARK\*STC  
J8 XX-JH  
NOT IOCE MCH CK  
INTR AND RESPONSE  
STC 0

XX 0B67  
S SET-MARK\*J61  
C STC+1  
J9 XX-JI  
SET MARKS 4-7

00 0B54  
A B=1  
A 0→R  
C 0→STC  
R RELI/O  
N6 XX-NF  
DECREMENT COUNT  
BR ON RESPONSE

X1 0B53  
A 0+1  
A 0→B  
L R→24+T  
C 0→INTR-GATE  
C 0→INT-TGR  
R STAE 0→DIERR  
N7 XX-NG  
RESPONSE RECEIVED  
IC TO T REG  
TOF SELECT  
BR ON IOCE MCH  
CHECK INTERRUPT  
OR ERROR FROM  
IOCE IF NORMAL  
I/O INTERRUPT  
(STAE)

10 0B62  
R 0  
N8 0X-NH  
NOT IOCE MCH CK  
INTR AND NO RESPONSE.  
DELAY TO ALLOW INTR  
REQUEST TGR TO DROP  
BEFORE END OP.

11 0B63  
S MS-REQ→D=4  
C INH-MS-PROT  
R 0  
N8 0X-NH  
IOCE MCH CK INTR.

01 0B55  
L R→24+T  
C 0→INTR-GATE  
C 0→INT-TGR  
R 1 0→DIERR  
Q6 1\*→QF  
TIMEDUT COMPLETE  
IC TO T REG  
TOF SELECT  
BR ON IOCE MCH CK.

01 0B55  
L R→24+T  
C 0→INTR-GATE  
C 0→INT-TGR  
R 1 0→DIERR  
Q6 1\*→QF  
TIMEDUT COMPLETE  
IC TO T REG  
TOF SELECT  
BR ON IOCE MCH CK.

01 0B55  
L R→24+T  
C 0→INTR-GATE  
C 0→INT-TGR  
R 1 0→DIERR  
Q6 1\*→QF  
TIMEDUT COMPLETE  
IC TO T REG  
TOF SELECT  
BR ON IOCE MCH CK.

01 0B55  
L R→24+T  
C 0→INTR-GATE  
C 0→INT-TGR  
R 1 0→DIERR  
Q6 1\*→QF  
TIMEDUT COMPLETE  
IC TO T REG  
TOF SELECT  
BR ON IOCE MCH CK.

01 0B55  
L R→24+T  
C 0→INTR-GATE  
C 0→INT-TGR  
R 1 0→DIERR  
Q6 1\*→QF  
TIMEDUT COMPLETE  
IC TO T REG  
TOF SELECT  
BR ON IOCE MCH CK.

QT041 AIE  
(XX)

QJ031 EIE  
(0A)

QQ031 NFE  
(0X) TO END OP

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